

BOX PCT

IN THE UNITED STATES DESIGNATED/ELECTED OFFICE
OF THE UNITED STATES PATENT AND TRADEMARK OFFICE
UNDER THE PATENT COOPERATION TREATY--CHAPTER I

5 APPLICANT: STEFAN PFAB
ATTORNEY DOCKET NO.: P00,0365
INTERNATIONAL APPLICATION NO: PCT/DE99/02009
INTERNATIONAL FILING DATE: 01 JULY 1999
INVENTION: "DATA STORAGE DEVICE"

10 Assistant Commissioner for Patents,
Washington D.C. 20231

INFORMATION DISCLOSURE STATEMENT

According to 37 C.F.R. §1.97(b)

Sir:

15 In accordance with the provisions of 37 C.F.R. §1.56 and the requirements of 37 C.F.R. §1.98, Applicant respectfully requests that a citation and examination of the references identified on the attached PTO 1449 form be made during the course of examination of the above-identified application for United States Patent.

20 The present Information Disclosure Statement is being filed according to
37 C.F.R. §1.97(b) and before the latter occurrence of:

- 25
- (1) three months from the filing date of a national application;
 - (2) three months from the date of entry of the national stage as set forth in 37 C.F.R. §1.491 in an international application;
 - or
 - (3) the mailing date of a first Office Action on the merits.

REMARKS

The attached PTO 1449 form lists prior art references identified in the International Search Report and other relevant art, copy of which is enclosed herewith.

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| 5 | AA | U.S. Patent 4,467,443 |
| | AB | U.S. Patent 5,307,321 |
| | AE | International application WO 95/22791 |
| | AF | European reference EP 0 217 232 A1 |
| | AM | H. P. Messmer, "PC-Hardwaarebuch", 5. Auflage, Addison- |
| 10 | | Wesley, (1997), pages 55, 56 |
| | AN | Intel, "Microprocessor and Peripheral Handbook, Volume 1, Microprocessor (1988) page 4-121 |

EXPLANATION OF RELEVANCE

References AA, AB, AE, and AF were cited in the International Search Report. References AA, AB, AE, and AN are in English, so no further commentary on their teachings is required. References AF and AM are in German. Reference AM discusses the memory segmentation and addressing scheme of the Intel 80x86 CPU. Reference AF discusses a circuit arrangement for splitting addresses in a data block. An English translation of the abstract of AF has been provided. A translation of the full text of references AF and AM will be provided to the Examiner upon request.

The filing of the present Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed as an admission that the information cited in the present Information Disclosure Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

The above citation of prior art is not a representation that such art constitutes a complete or exhaustive listing of all pertinent prior art, nor that it necessarily includes the closest or most relevant art. The aforementioned citation comprises a voluntary citation of prior art of which applicant and his attorney are presently aware and is not intended to serve as a substitute for the Examiner's own search.

Submitted by,

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